TrustZone for Armv8-A

Summary:

This course has been designed to provide an insight into the purpose and functionality of Arm's TrustZone from Armv8-A technology.

At the end of the session, participants will be able to explain some of the use cases for TrustZone and how the architectural features aid these goals. Participants will be able understand the software isolation and partition models used by TrustZone both in the processor and wider memory system.

Prerequisites:

- Basic knowledge of System-on-Chip architectures
- Familiarity with the Armv8-A architecture is not required but would be beneficial

Audience:

Engineers and system Designers who are seeking a high-level overview of TrustZone from Armv8-A.

Delivery method:

- Online

Length:

1 hour